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UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Koichiro Tanaka
Serial No. : 09/842,797
Filed : April 27, 2001
Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Art Unit : 2822
Examiner : Maria F. Guerrero

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	52	-	30	=	22	\$396
Independent	10	-	6	=	4	\$344
First Presentation of Multiple Dependent Claims						\$0

Applicant hereby petitions under 37 C.F.R. §1.136 for a 1 month extension of time.

\$110

TOTAL FEE DUE

\$850

A check for \$850 is attached.

Respectfully submitted,

Date: 9/21/04


John F. Hayden
Reg. No. 37,640

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